



Data Sheet DMM-3537-B

Features:

The DMM-2735-B digital MEMS microphone features a specialized preamplification ASIC that provides high sensitivity and high SNR output from a capacitive audio sensor. It's packaged for surface mounting and high temperature reflow assembly. The digital data format is single-bit PDM.

- -37dBFS sensitivity
- 67dB Signal-to-Noise
- Digital PDM output
- Small 2.65mm x 3.5mm surface-mount package

Specifications ($f_{CLOCK} = 2.4MHz$, $V_{DD} = 1.8V$, unless otherwise specified.)

Parameter	Test Condition	Value	Unit
	94dBSPL	-38 (min)	
Sensitivity	$f_{IN} = 1 \text{ kHz}$	-37 (typ)	dBFS
	All operating modes	-36 (max)	
Supply Voltage		1.8 (typ)	V_{DD}
Supply Voltage Range		1.6 (min) 3.3 (max)	V_{DD}
Supply Current	$V_{DD} = 1.8V$ $f_{SAMPLE} = 3.072MHz$	1050 (typ) 1150 (max)	μA
Signal-to-Noise Ratio	f _{IN} = 1kHz 94dBSPL A-weighted	67 (typ)	dB
Frequency Range	See Frequency Response Curve for response limits	20 – 20k (typ)	Hz
Total Harmonic Distortion	$f_{IN} = 1 \text{kHz}$ 94dBSPL	0.5 (max)	%
Acoustic Overload Point (AOP)	f _{IN} = 1kHz 10% THD	130 (typ)	dB
Power Supply Rejection	100mV _{PP} 217 Hz square wave on V _{DD} A-weighted	-90 (typ)	dB
Phase Response	94dBSPL 50Hz < fIN < 2000Hz	-5 (min) 5 (max)	0

Specifications (f_{CLOCK} = 768kHz, V_{DD} = 1.8V, unless otherwise specified.)

Parameter	Test Condition	Value	Unit
	94dBSPL	-22 (min)	
Sensitivity	$f_{IN} = 1 \text{ kHz}$	-21 (typ)	dBFS
	All operating modes	-20 (max)	
Supply Voltage		1.8 (typ)	V_{DD}
Supply Voltage Pange		1.6 (min)	\/
Supply Voltage Range		3.3 (max)	V_{DD}
Supply Current	$V_{DD} = 1.8V$	310 (typ)	
Supply Current	f _{SAMPLE} = 768kHz	410 (max)	μA
o: 11 11 : 5 !:	$f_{IN} = 1 \text{kHz}, 94 \text{dBSPL}$	(7 (t) (p)	dB
Signal-to-Noise Ratio	A-weighted	67 (typ)	
Frequency Range	See Frequency Response Curve for response limits	20 – 20k(typ)	Hz
Total Harmonic Distortion	$f_{IN} = 1 kHz$	1.0 (max)	%
	94dBSPL	1.0 (11107)	/0
Acoustic Overload Point	$f_{IN} = 1 \text{kHz}$	117 (typ)	dB
(AOP)	10% THD	117 (190)	аь
Power Supply Rejection	100mV_{PP} 217 Hz square wave on V_{DD} , A-weighted	-75 (typ)	dB

Physical Properties

Parameter	Condition	Value Unit	
Directivity		Omnidirectional	
Weight		0.3 (max) Grams	
Operating Temperature		-40 (min) 85 (max)	°C
Storage Temperature		-40 (min) 100 (max)	°C
MSL (Moisture Sensitivity Level)*		Class 1	
Acceptable Soldering Methods		See page 3 for reflow soldering information	
Environmental Compliances		RoHS/Halogen Free	

^{*}MSL level dependent on product remaining in sealed packaging until use

Operating Ratings

Parameter	Test Condition	Value	Unit
		1.6 (min)	
Power Supply Voltage (V _{DD})		1.8 (typ)	V
		3.3 (max)	
	Sleep Mode	320 (max)	kHz
		450 (min)	
Clock Frequency Range	Lower Power Mode	768 (typ)	kHz
(fclock)		850 (max)	
(ICLOCK)		1.2 (min)	
	Standard Mode	3.072 (typ)	MHz
		3.3 (max)	
Clock Duty Cycle		45 (min)	%
Clock Duty Cycle		55 (max)	%
		0.65•V _{DD}	
Input Logic High Level		(min)	
inportogic riigir tevel		V _{DD} +0.3	
		(max)	V
		-0.3 (min)	
Input Logic Low Level		0.28 • V _{DD}	
		(max)	
Output Logic High Level		0.7 • V _{DD} (min)	
Output Logic Low Level		0.3•V _{DD}	V
Colpoi Logic Low Level		(max)	
Output Logic Load Capacitance		200 (max)	рF
Data Valid Time		20 (max)	ms
Time After Stable Clock to Achieve Specified Sensitivity	Sensitivity, ±0.2dB	20 (min)	ms

Timing Characteristics

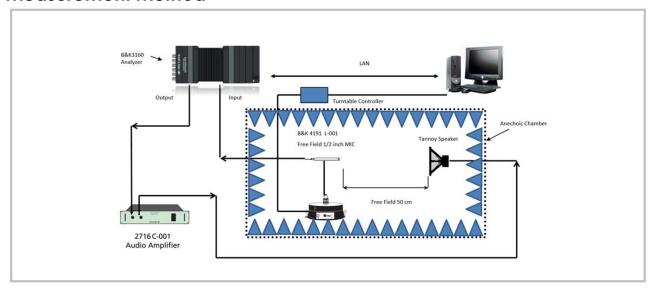
Parameter	Test Condition	Value	Unit
Clock Timing Characteristics			
Clock Duty Cycle (DC _{CLOCK})		45 (min) 55 (max)	%
Clock Rise Time (tcr)	10% to 90%	13 (max)	ns
Clock Fall Time (t _{CF})	90% to 10%	13 (max)	ns
Data Timing Characteristics			
Time Delay Between Clock Edge and Data Line Driven [DV _{DD} Mode] (t _{DD_DVDD})	DV _{DD} Digital Interface	28 (min)	ns
Time Delay Between Clock Edge and Data Line Driven [Internal 1.2V Mode] (†DD_1V2IO)	Internal 1.2V Digital Interface	24 (min)	ns
Time Delay to Valid Data [Normal Mode] (t _{DV_NM})	DV _{DD} Digital Interface: f _{CLOCK} = 768kHz, 2.0MHz, 3.072MHz, or 4.0MHz Internal 1.2V Digital Interface: f _{CLOCK} = 2.0MHz, 3.072MHz, or 4.0MHz	100 (max)	ns
Time Delay to Valid Data [Low Power or Internal 1.2V Mode] (tdv_LPM_1V2IO)	Low Power Mode or Internal 1.2V Mode Digital Interface: fclock = 768kHz	185 (max)	ns
Time Delay to Valid Data [Ultrasonic Mode] (t _{DV_UM})	Ultrasonic Mode: fclock = 4.8MHz	80 (max)	ns
Time Delay to High Impedance [DV _{DD} Mode] (t _{HZ_DVDD})	DV _{DD} Digital Interface	14 (min) 26 (min)	ns
Time Delay to High Impedance [Internal 1.2V Mode] (thz_1v2io)	Internal 1.2V Digital Interface	14 (min) 22 (min)	ns

Absolute Maximum Ratings

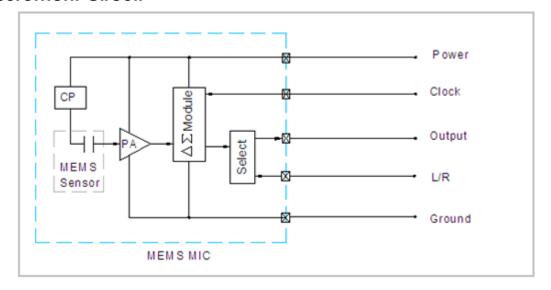
Parameter	Condition	Value	Unit
Max Voltage on Any Pin		3.3	V_{DC}
Voltage on any Pin		-0.3 (min) V _{DD} + 0.3	V
Max Sound Pressure Level		160	dB
Max Mechanical Shock		10000	G
Max Vibration		Pre-MIL-STD-883 Method 2007, Test Condition B	

©2023, PUI Audio Inc.

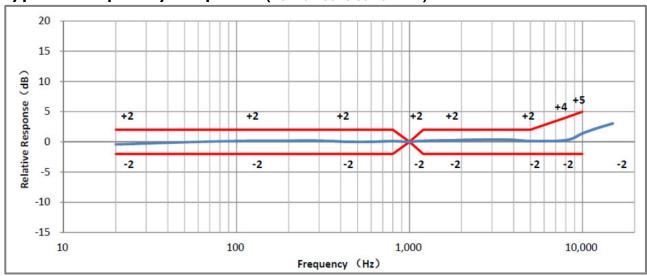
Measurement Method



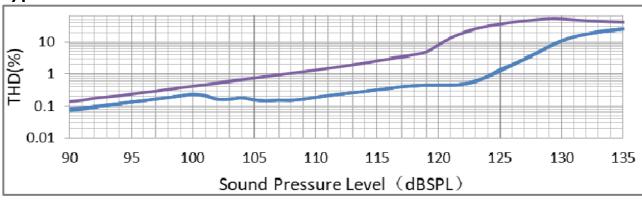
Measurement Circuit



Typical Frequency Response (Normalized to OdB at 1kHz)

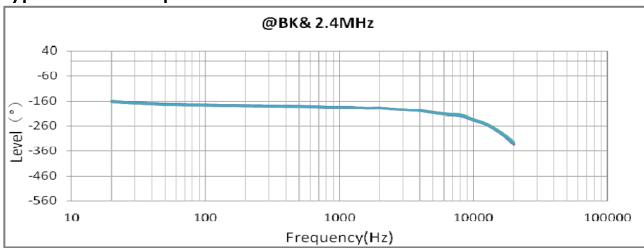


Typical THD Vs SPL

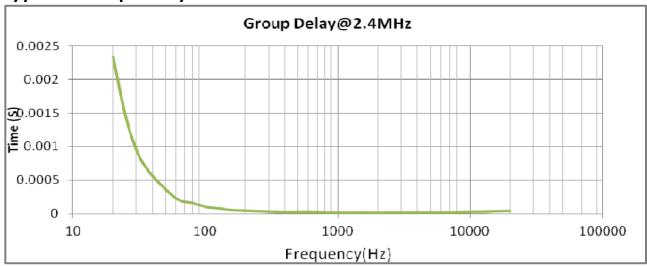


 f_{CLOCK} : ____ = 3.072MHz; ____ = 2.400MHz; ____ = 1.536MHz; ____ = 0.768MHz

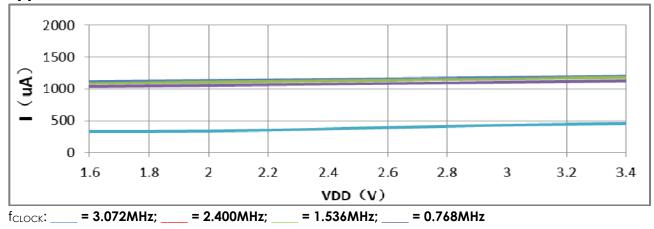
Typical Phase Response



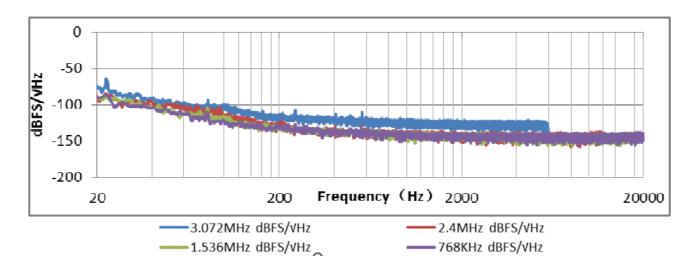
Typical Group Delay



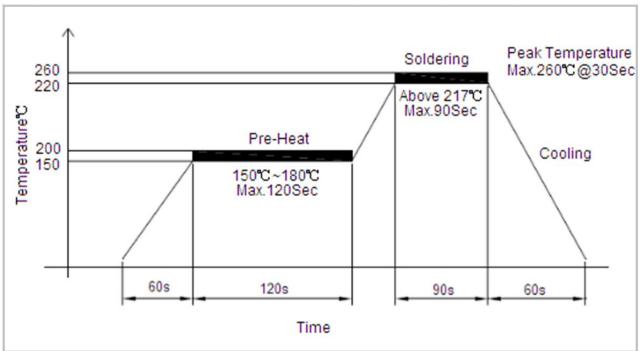
Typical I_{DD} vs. V_{DD}



Typical Noise Floor (Unweighted)



Recommended Reflow Soldering Procedure (Recommended profile, temperature ≤ 260°C, 30s maximum at peak temperature)



Important notes to minimize device damage

- 1. Do not handle the microphone with pick-and-place vacuum tools that could contact the microphone acoustic port hole.
- 2. Never expose the microphone's acoustic port hole to vacuum. Such exposure can damage or destroy the MEMS element.
- 3. Never allow air to blow air into the microphone acoustic port hole. The port hole must be sealed to prevent particle contamination if a blown air-cleaning process is used,
- 4. A clean room environment is recommended for PCB assembly to avoid microphone contamination.
- 5. Do not use blown air or ultrasonic cleaning procedures on MEMS Microphones. A noclean paste is recommended for the assembly, avoiding subsequent cleaning steps. cleaning substances can severely damage the microphone MEMS element.
- it is recommended to cover the sound port with protective tape during PCB sawing or system assembly. This prevents blocking or partially blocking the acoustic port hole during PCB assembly.
- 7. Do not use excessive force to place the microphone on the PCB. Use industry standard pick and place tools to limit the mechanical force exerted on the package.

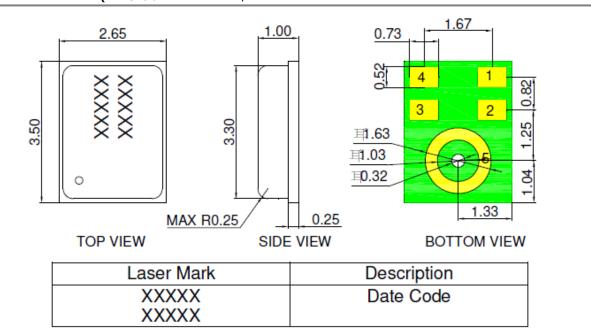
Reliability Testing (Samples under test are acclimated at $T_A = 23\pm2^{\circ}$ C, R.H. = 55 \pm 10% for two hours. After each test completes and corresponding recovery time (if applicable) elapses, any measured sensitivity change is \leq \pm 3dB, unless otherwise specified)

Type of Test	Test Specifications
High Temperature Storage Test	1000hrs at 105±3°C, two-hour recovery

High Temperature Operational Test	1000hrs at 105 \pm 3°C, $V_{DD} = V_{DD}$ (max), four-		
riigit terriperatore Operational test	hour recovery		
Low Temperature Storage Test	1000hrs at -40±3°C, two-hour recovery		
Low Temperature Operational Test	1000hrs at -40 \pm 3°C, $V_{DD} = V_{DD}$ (max), four-		
Low remperatore operational rest	hour recovery		
High Humidity, High Temperature	1000hrs at 85 \pm 3°C and 85%RH, $V_{DD} = V_{DD}$		
Operating Test	(max), twelve-hour recovery, no corrosion		
Operating rest	or defamation inside the microphone		
High Humidity, High Temperature	168hrs at 65 \pm 3°C and 95%RH, $V_{DD} = V_{DD}$		
Operating Test	(max), twelve-hour recovery, no corrosion		
Operating rest	or defamation inside the microphone		
	Double-case method:		
	15min at -40±3°C		
Temperature-Cycle Testing	Followed by		
	15min at 125±3°C		
	100 cycles, two-hour recovery		
	Twelve minutes along the x, y, and z axis		
	$f_{IN} = 20$ Hz to 2kHz		
Vibration Test	20G peak acceleration		
	Two-hour recovery		
	Less than 1dB sensitivity change		
	Height: 1.5m		
	Fixture weight: 150±10g		
	Fixture's sound hole diameter is ≥0.8mm		
Shock Test	Reference surface is marble floor		
	Duration: four corners x four times; six faces		
	x four times		
	Less than 1dB sensitivity change		
	Describe distance, drop surface material,		
Drop Test	and number of test cycles/how many		
	sides are tested.		

©2023, PUI Audio Inc.

Dimensions (Dimension are in mm.)



Item	Dimension	Tolerance(+/-)	Units
Length(L)	3.50	0.10	mm
Width(W)	2.65	0.10	mm
Height(H)	1.00	0.10	mm
Acoustic Port(AP)	Ø0.32	0.05	mm

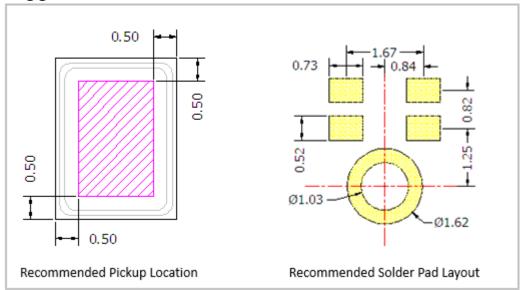
Pin #	Pin Name	Туре	Description	
1	Output	Signal	Output Signal	
2	L/R	L/R Channel	Channel select	
3	CLK	Clock	Clock input	
4	V _{DD}	Power	Power Supply	
5	GND	Ground	Ground	

Notes:

All dimensions are in millimeter (mm).

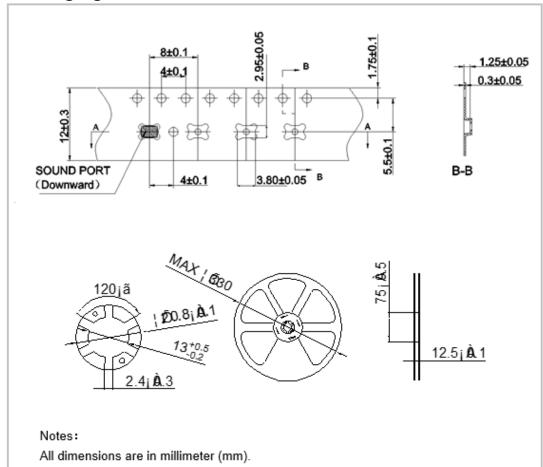
Tolerance±0.15mm unless otherwise specified.

Suggested Land Pattern*

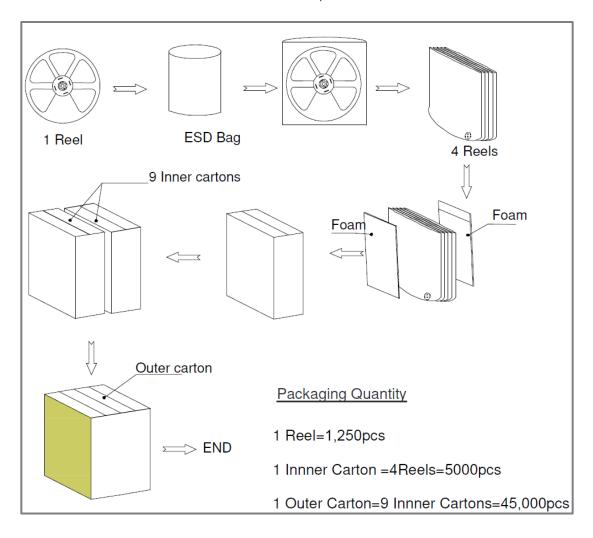


^{*}This land pattern is advisory only and its use or adaptation is entirely voluntary. PUI Audio disclaims all liability of any kind associated with the use, application, or adaptation of this land pattern.

Packaging



©2023, PUI Audio Inc.



Specifications Revisions

epodinound its revisions		
Revision	Description	Date
Α	Released from Engineering	03-16-2023

Note:

- 1. Unless otherwise specified:
 - A. All dimensions are in millimeters.
 - B. Default tolerances are ± 0.5 mm and angles are $\pm 3^{\circ}$.
- 2. Specifications subject to change or withdrawal without notice.
- 3. This part is ROHS 2015/863/EU compliant.